IITPKD/R/NF/02/2024/19   **09ᵗʰ Dec 2024**

Recruitment for the post of Junior Technical Superintendent (Computer Science) (Post Code: 24208) vide Advt. No.: IITPKD/R/NF/02/2024 dated February 07, 2024.

The Level I selection process, indicative syllabus and date of examination for the post of Junior Technical Superintendent (Computer Science) advertised vide Advt. No.: IITPKD/R/NF/02/2024 dated February 07, 2024.

Junior Technical Superintendent (Computer Science)

The Level I selection process shall consist of a screening test (computer based), the syllabus of which is given below:

Syllabus:

Subject		Time
Level I (screening test)	Basic computer science and Engg inclusive of programming, operating system data and file structure, system software, virtualisation and cloud computing, network and computer hardware, system administration, software engineering and DBMS at UG/PG/MCA level.	1.5 hrs

Tentative Schedule of Examination: 17-Dec-2024

The details of venue, time and other instructions shall be shared along with the hallticket that will be issued to the shortlisted candidates over email in due course.

-Sd-
Registrar